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APPLICATION NO./ CONTROL NO.	FILING DATE	FIRST NAMED INVENTOR / PATENT IN REEXAMINATION	ATTORNEY DOCKET NO.
09/479,375	01/05/2000	Perego, Richard E. , et al.	RA158

Rambus Inc.
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EXAMINER

ART UNIT

PAPER

DATE MAILED: 11/30/2004

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Paper No. 15

Rambus Inc.
4440 El Camino Real
Los Altos, California 94022

In re U.S. Patent of: Richard E. Perego et al.)
Application No. 09/479,375)
Filed: January 5, 2000)
U.S. Patent No. 6,502,161)
Issued: December 31, 2002)

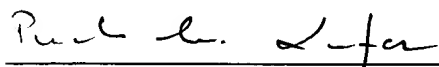
**NOTIFICATION UNDER 37
C.F.R. § 1.607(d) OF AN
ATTEMPT TO PROVOKE AN
INTERFERENCE**

You are hereby notified under 37 CFR 1.607(d) that an applicant is seeking to provoke an interference with your U.S. Patent No. 6,502,161 entitled "Memory System Including a Point-to-Point Linked Memory Subsystem".

The application in which claims have been copied is Serial No. 10/747,820 which is a Reissue application of U.S. Patent No. 6,526,469 issued to Robert Drehmel et al. on February 25, 2003.

If a final decision is made not to declare an interference, a notice to that effect will be placed in the patent file and will be sent to the patentee.

If an interference is declared, notice thereof will be made under 37 CFR 1.611.



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